

PATENT

Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Louis L. HSU, et al. GROUP ART UNIT: 2814  
APPLN NO: 09/880,223 EXAMINER: Rao, Shrinivas  
FILING DATE: June 13, 2001 DATED: October 8, 2004  
FOR: T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR  
AND A PSEUDO-TFT TRANSFER GATE AND A METHOD  
FOR FABRICATING THE SAME

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

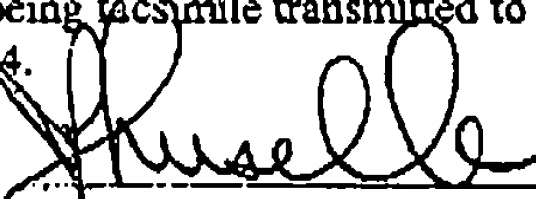
Sir:

In response to the Office Action of the United States Patent and Trademark Office mailed July 28, 2004, please consider the following amendments and remarks.

**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office (571) 273-1718 on October 8, 2004.

Dated: October 8, 2004

  
Michael J. Musella